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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Rupal Parikh

Serial No.: 10/038,956

Filed: January 2, 2002

For: Synchronizing Data or Signal  
Transfer Across Clocked  
Logic Domains

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Art Unit: 2634

Examiner: Linda Wong

Docket: ITL.0654US  
P12395

Assignee: Intel Corporation

Mail Stop **Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO PAPER NO. 1**

Sir:

In response to the office action mailed May 5, 2005, reconsideration is requested in view of the following remarks.

Date of Deposit: July 5, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

*Cynthia L. Hayden*  
Cynthia L. Hayden